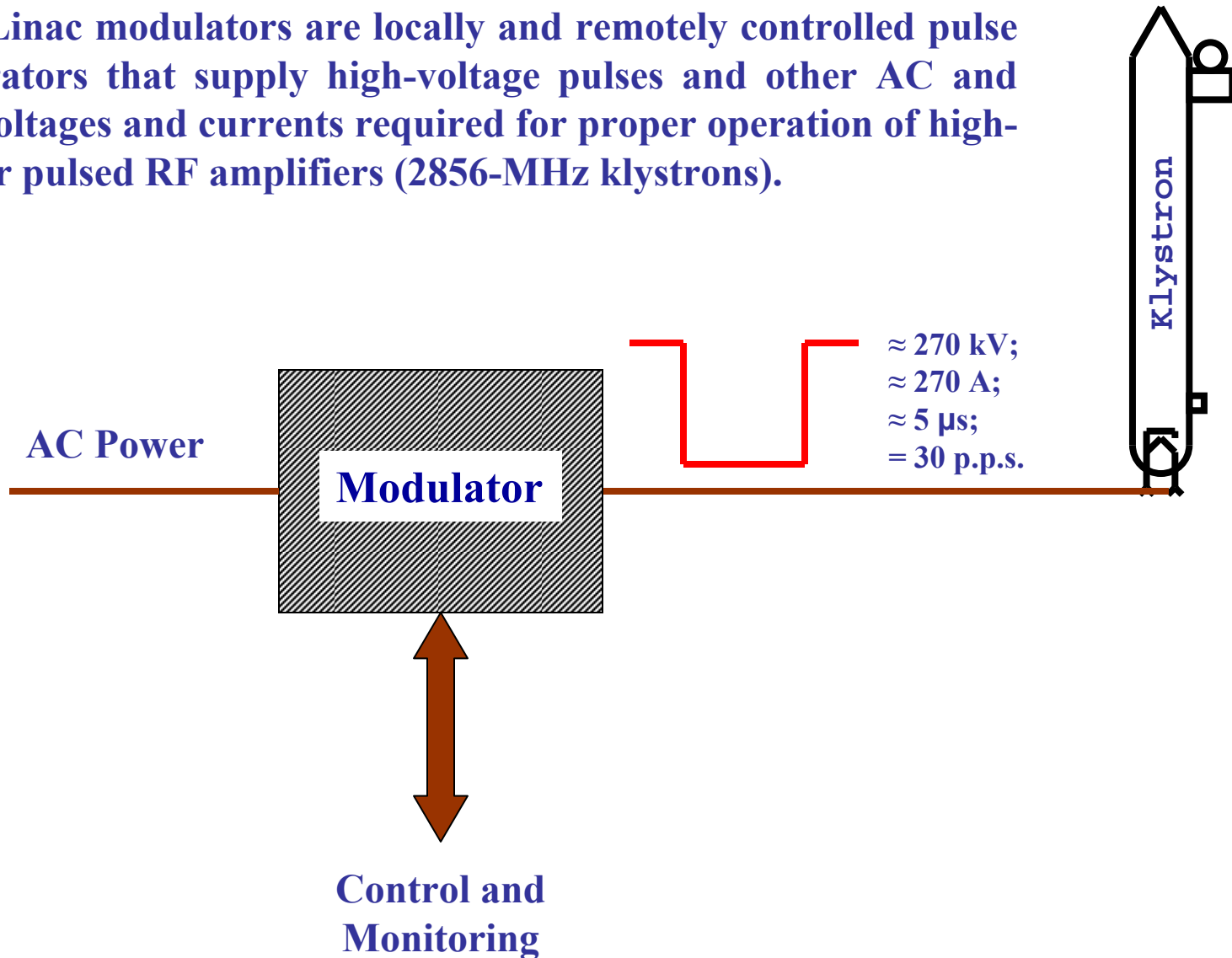


# **The Linac Modulator**

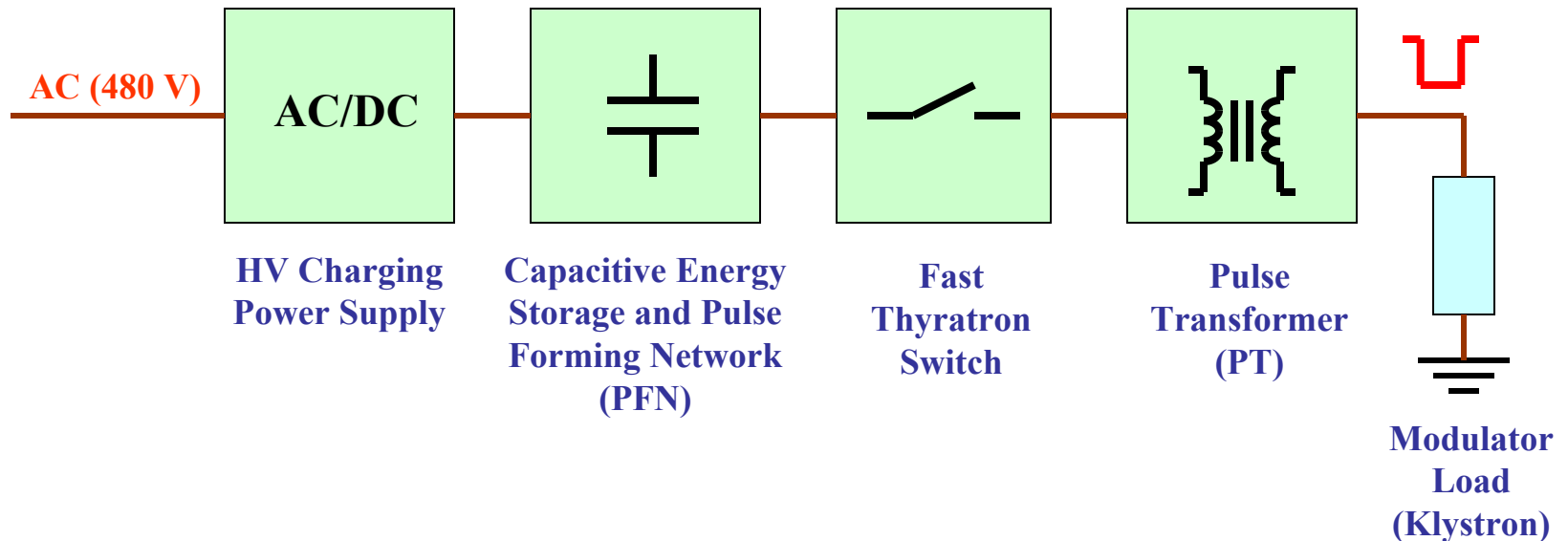
- 1. Introduction**
- 2. Block Diagram**
- 3. Charging PFN Capacitors**
- 4. Dumping the PFN**
- 5. Monitoring PFN Voltage**
- 6. The Summing Box**
- 7. Pulsing**
- 8. Forming the Pulse**
- 9. PFN Impedance**
- 10. Secondary Pulse Shape**
- 11. End of Line Clipper**
- 12. Auxiliary Power Supplies**
- 13. Modulator Control**

# The Linac Modulator. Introduction

The Linac modulators are locally and remotely controlled pulse generators that supply high-voltage pulses and other AC and DC voltages and currents required for proper operation of high-power pulsed RF amplifiers (2856-MHz klystrons).

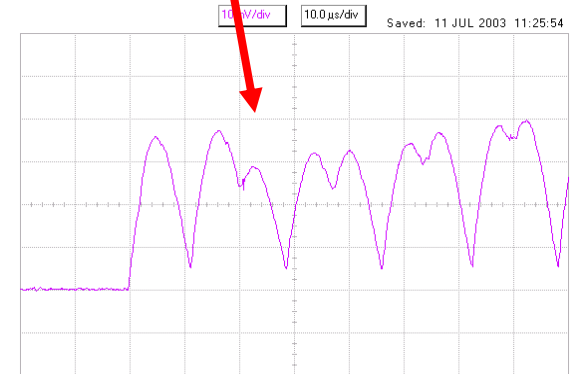
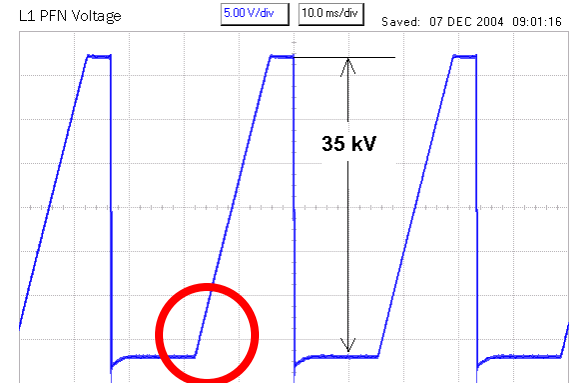
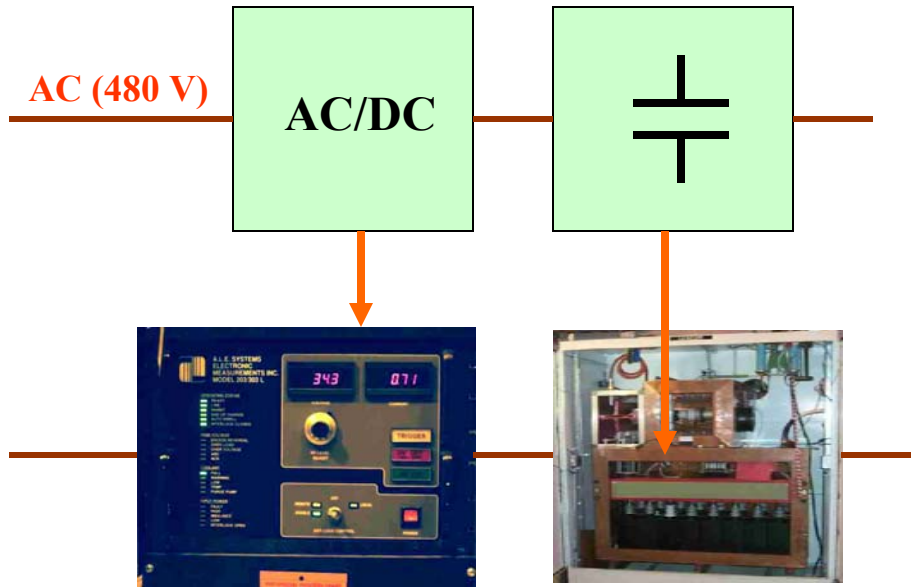


## The Linac Modulator. Block Diagram



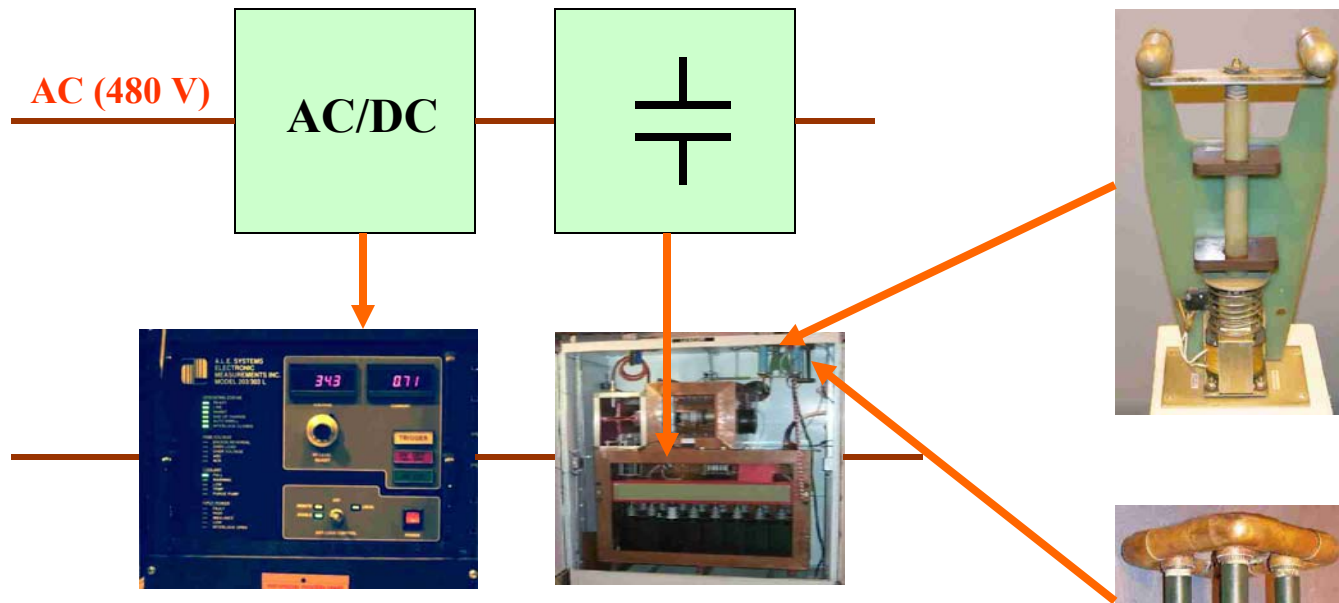
- ◆ All six Linac modulators are identical
- ◆ Lambda/EMI 40-kV power supplies are used to charge PFN capacitors
- ◆ E2V thyatrons act as closing switches
- ◆ Pulse transformers (PTs) increase amplitude of the pulses by factor  $\approx 15.3$
- ◆ The pulses are delivered to the cathode of Klystron TH2128(D)

# The Linac Modulator. Charging PFN Capacitors



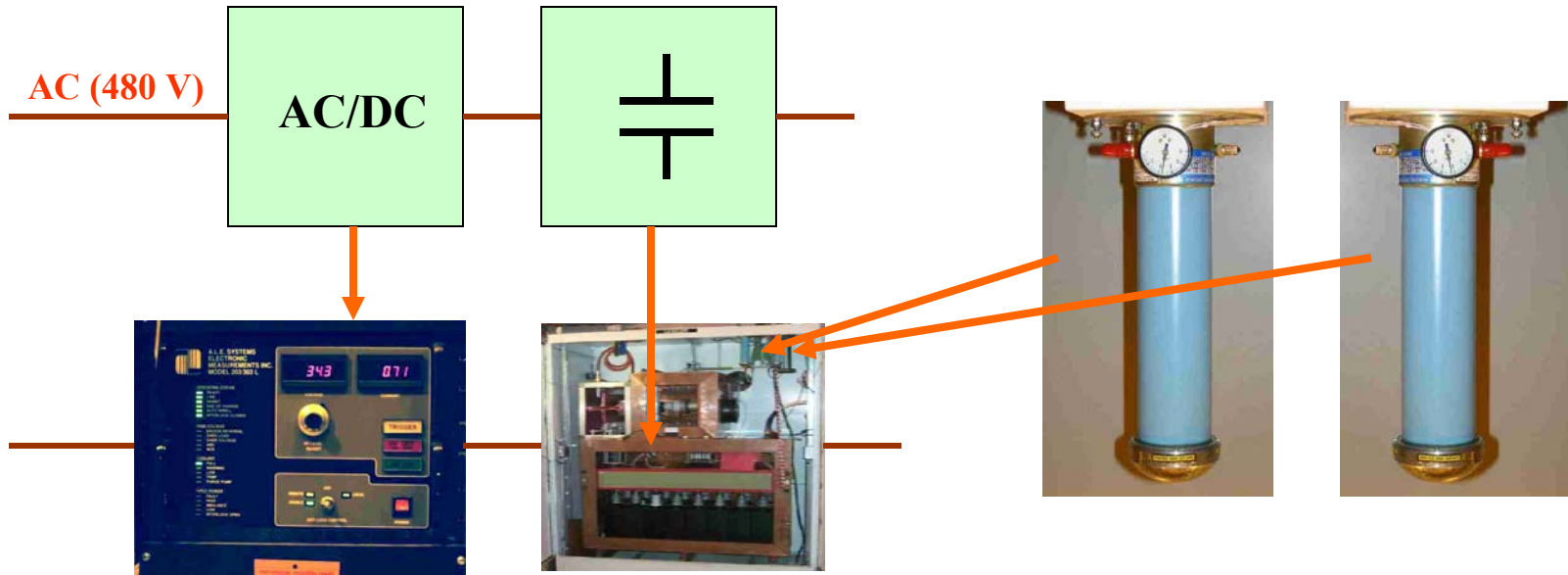
- **40-kV EMI power supply charges PFN capacitors during approximately 15 ms; then it maintains needed voltage level with  $\pm 0.1\%$  accuracy.**
- **During charging time, voltage slope is NOT a straight line; it consists of hundreds of steps or pulses following with about 50-kHz frequency.**
- **Average charging current is 0.7-0.8 Amps.**

# The Linac Modulator. Dumping the PFN



For safety reasons, when PFN cabinet doors are- or are to be open, remaining in the PFN capacitors energy is dumped through the 250-k $\Omega$  resistor assembly by de-energizing the Dumping switch

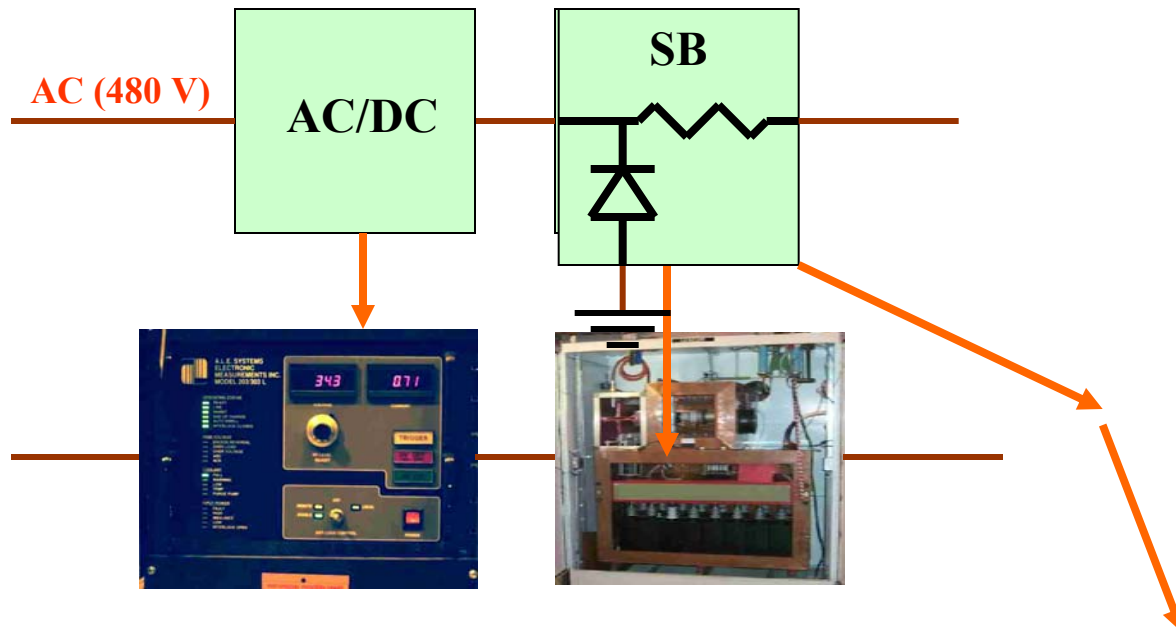
## The Linac Modulator. Monitoring PFN Voltage



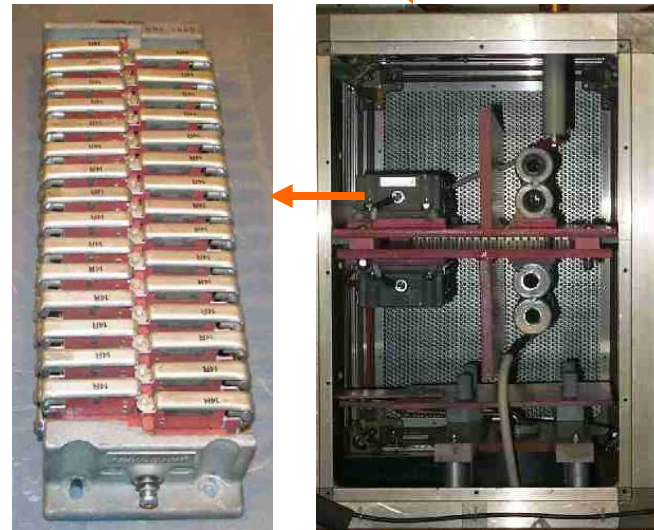
PFN Voltage is monitored by means of two Ross Voltage dividers (VD) filled with SF<sub>6</sub> isolating gas. Signal from one VD is sent to the modulator control system and used for read back purposes

The second VD is used only for local monitoring through the BNC connector located on the side wall of the PFN cabinet.

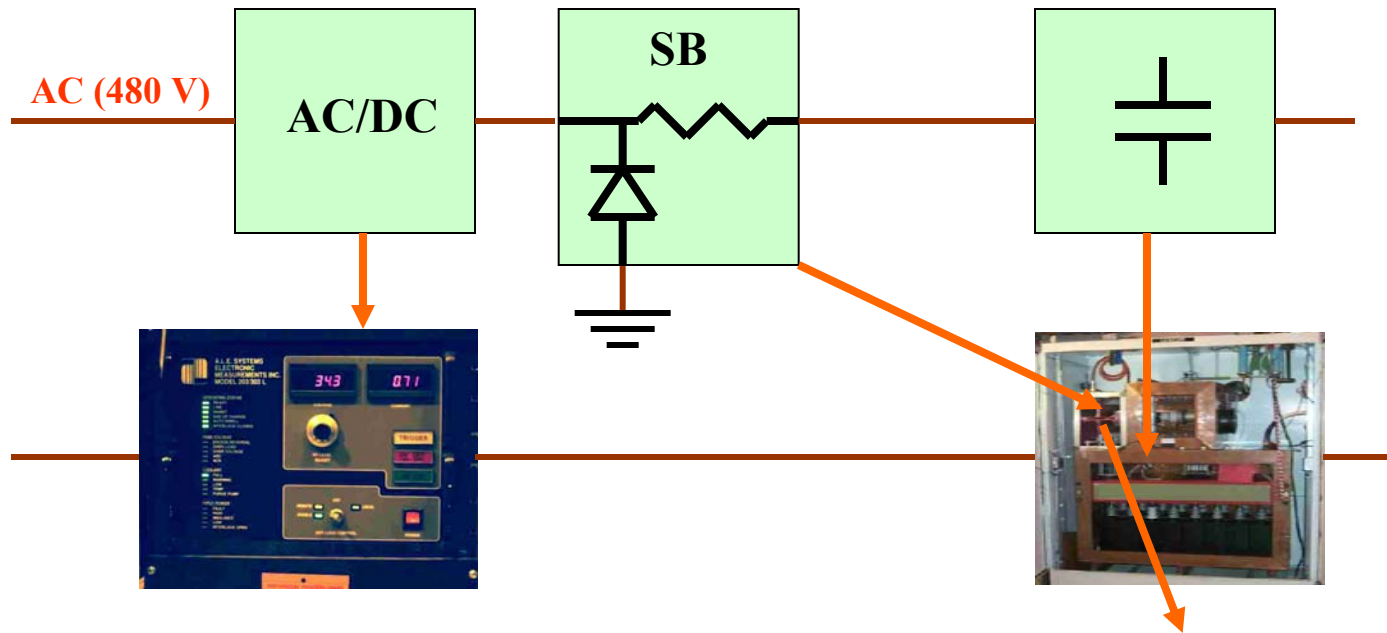
# The Linac Modulator. Summing Box (SB)



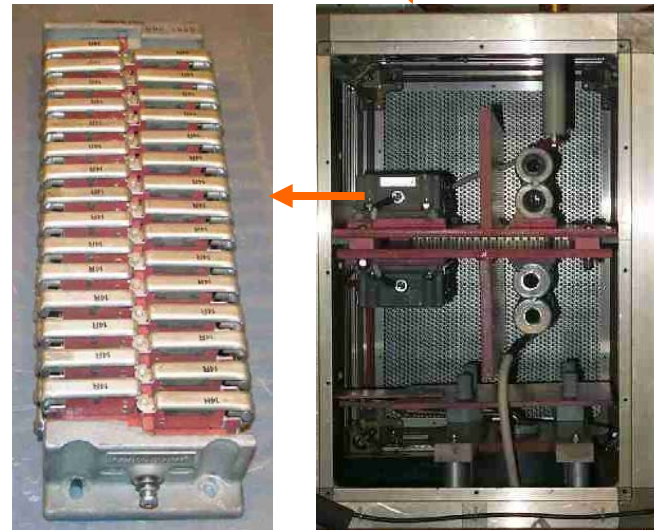
- The Summing Box (SB) consists of a HV diode assembly and four resistors connected in parallel with total resistance of  $50 \Omega$
- The SB protects EMI HV power supply against possible PFN voltage reversals, spikes, etc.



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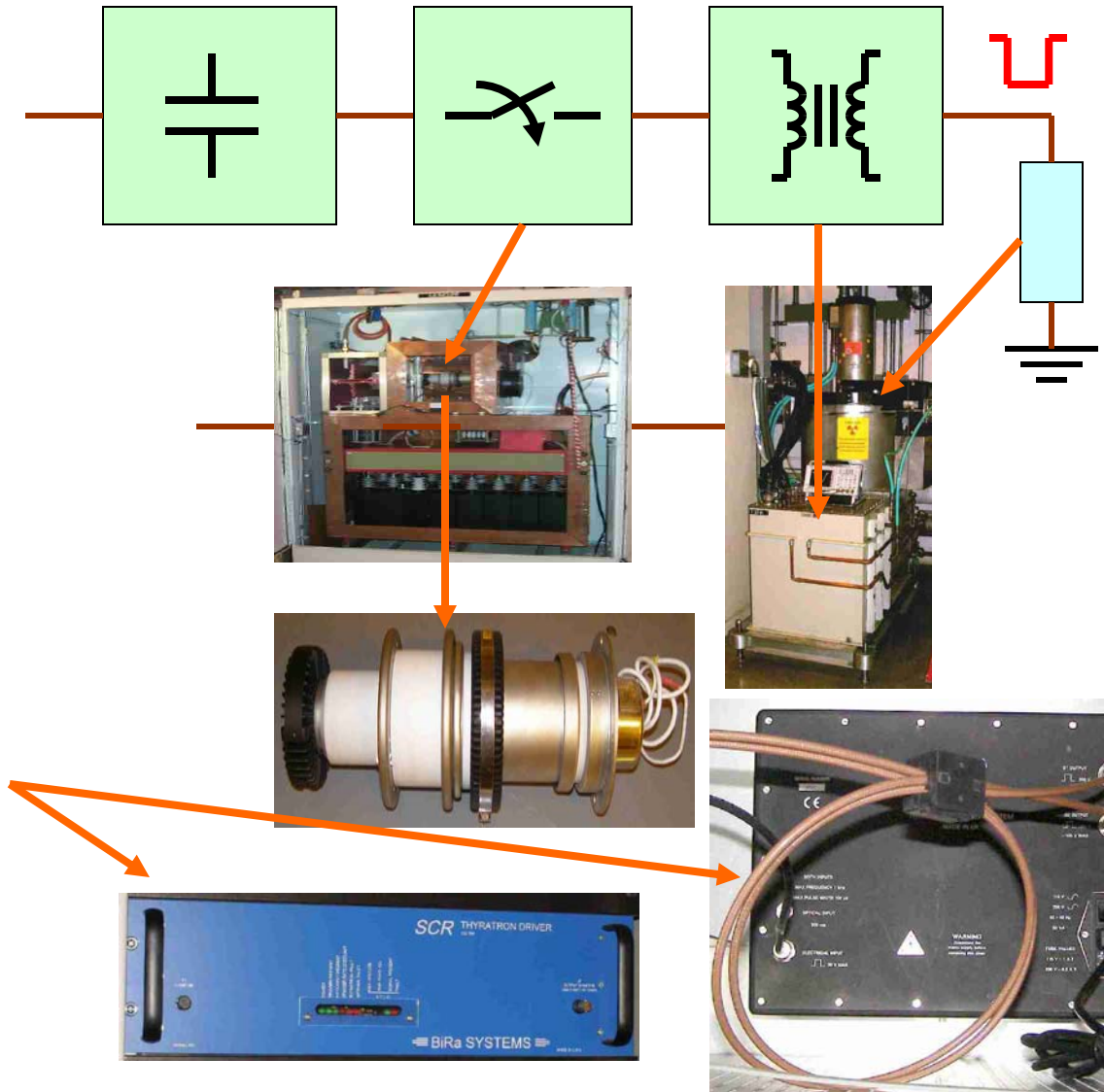


# The Linac Modulator. Pulsing

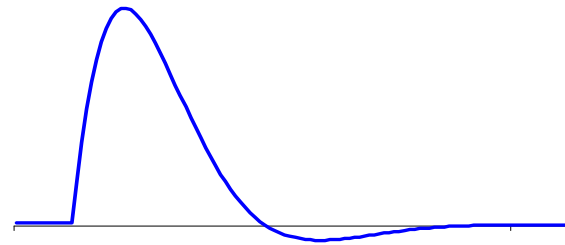
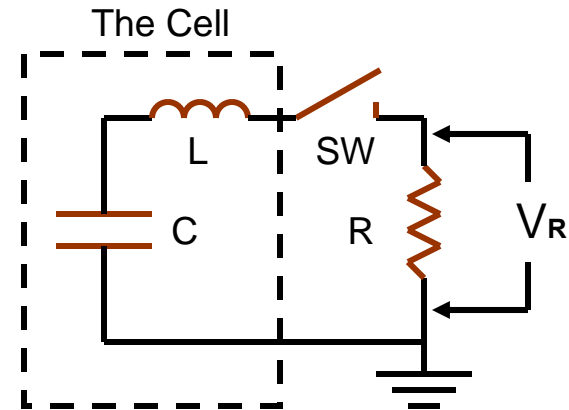
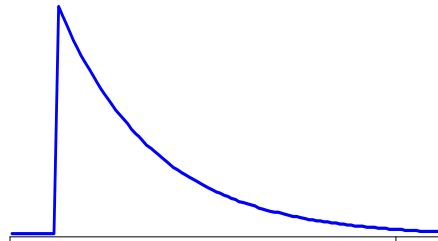
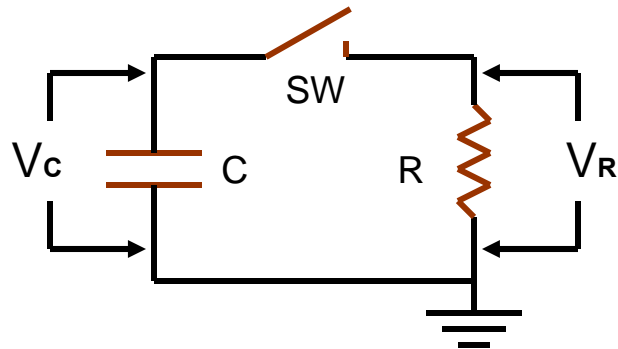
When the modulator receives “Discharge” command from the Linac Timing system, thyatron CX1836A turns into conducting state, and the energy that was previously accumulated in the PFN capacitors starts being transported to the load (the klystron) in form of a 5- $\mu$ s DC pulse.

The old and new thyatron drivers are shown on the photos at lower right.

The way the pulse is formed will be discussed later.



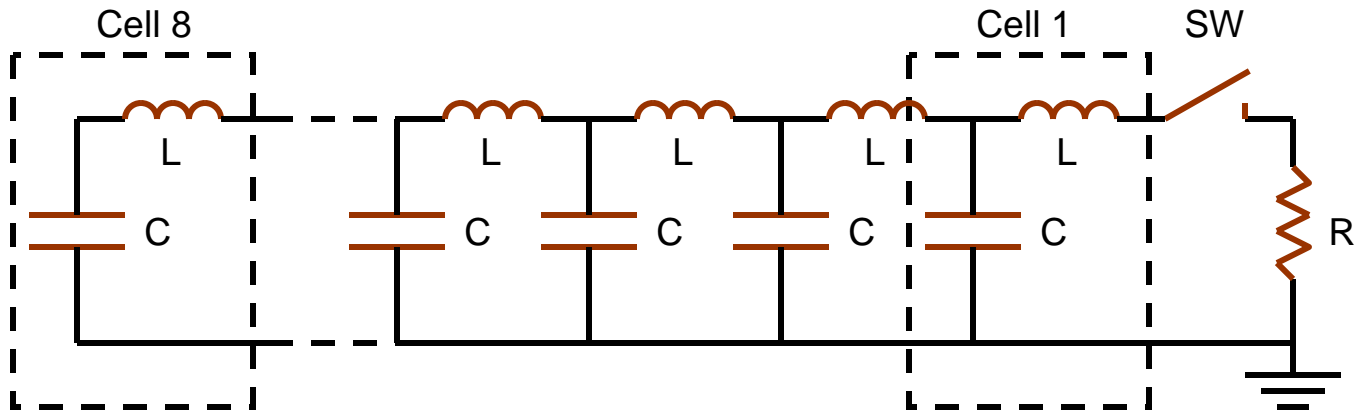
## Forming the Pulse. RC and RLC Pulses



If a capacitor is discharged into a resistive load directly, the pulse will have a very short front edge, no flat top, and a long exponential tail.

If a capacitor is discharged into a resistive load through a coil with a certain inductance  $L$ , the pulse will look almost like a more or less asymmetrical bell with no or little oscillation at its tail.

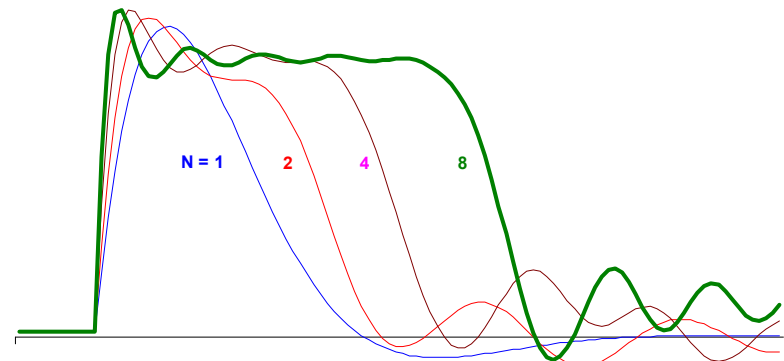
## From a Single Cell to the Pulse Forming Network (PFN)



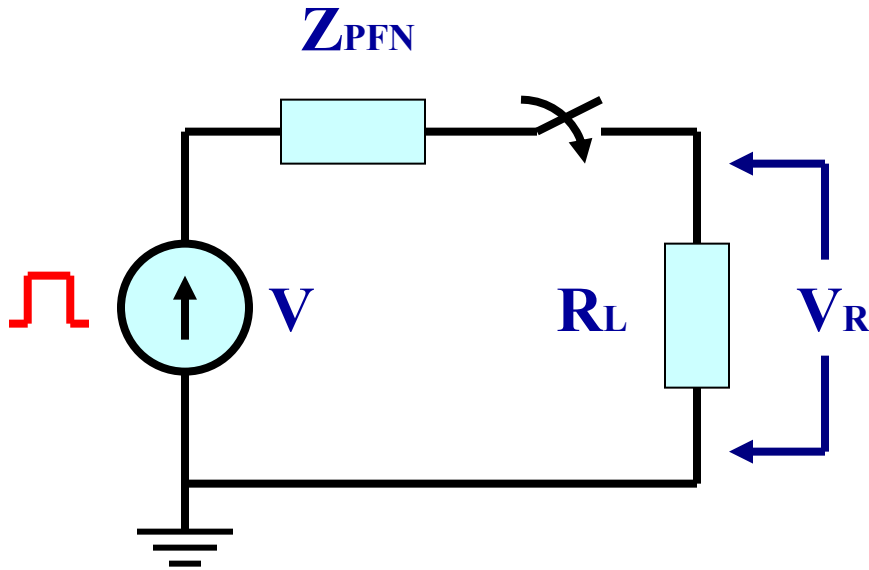
As the number of identical cells in the PFN grows, the pulse becomes wider and more square.

Initial overshooting and oscillation are inherent in this type of PFN.

They may be more or less successfully compensated by adjusting capacitance and/or inductance of certain cells.

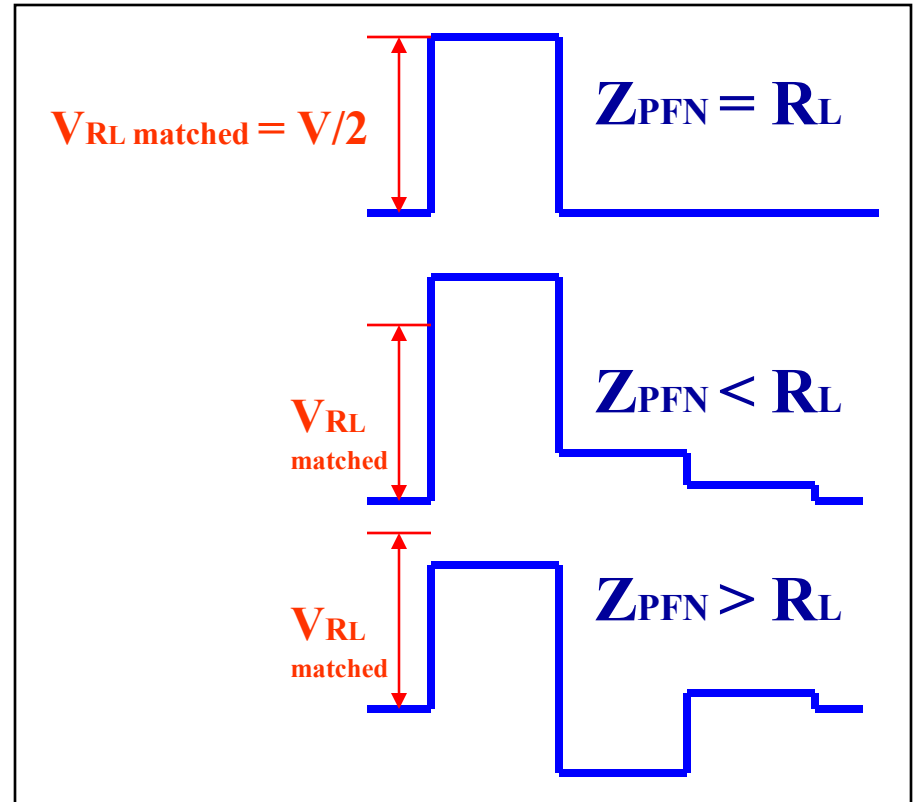


## PFN Impedance. PFN-to-Primary Voltage Ratio



$$Z_{PFN} \approx \sqrt{\frac{L_{PFN}}{C_{PFN}}}$$

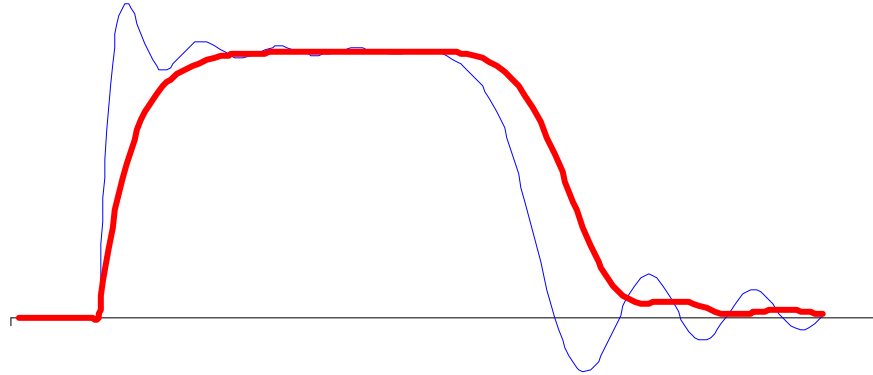
$$V_R = V \frac{R_L}{Z_{PFN} + R_L}$$



**Pulse Transformer Primary Voltage in matched and mismatched cases**

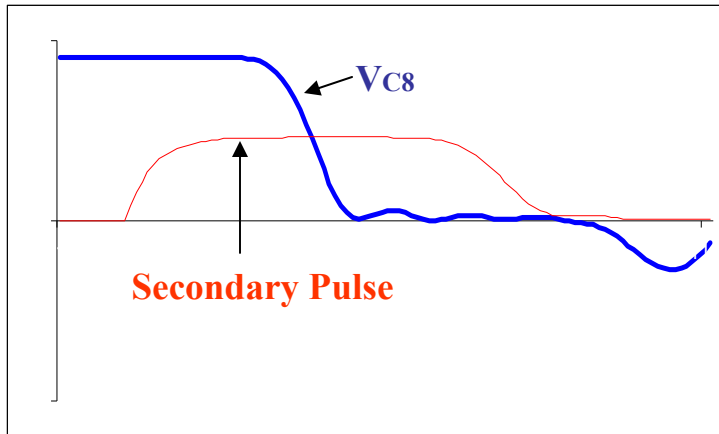
PFN Impedance  $Z_{PFN}$  must be close to impedance of the load (klystron) as seen from the primary side of the pulse transformer ( $\approx 4\Omega$ ). In other words, the PFN must be *matched* with the load. Otherwise, the pulse will have different amplitude and extra steps or reversals at the end; and a certain portion of energy will be lost.

## Pulse Shape on the Primary and Secondary Leads of the PT



- Because of stray inductance and capacitance inherent in various modulator components such as HV cables, PT, etc., “primary” (blue) and “secondary” (red) pulse shapes are different from each other.
- The “secondary” pulse is smoother, has longer leading and tail edges. Its top and tail have less ripples

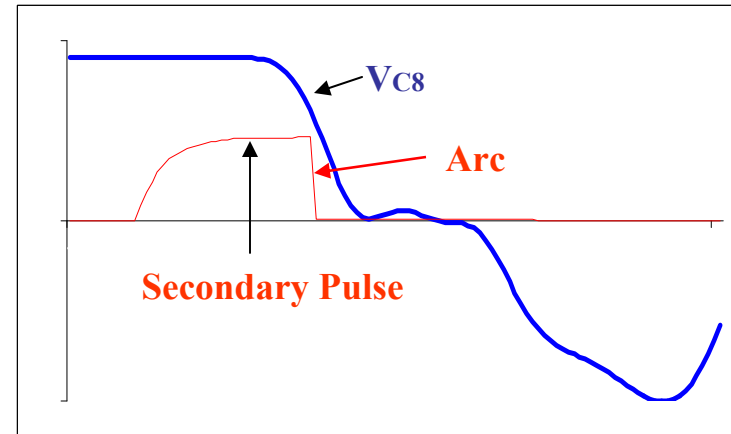
## End Of Line (EOL) Clipper



Voltage across the last capacitor of the PFN holds its charge during about one half of the pulse duration. After the pulse, an unused portion of energy comes back to the PFN. It results in recharging PFN capacitors to some negative voltage level.

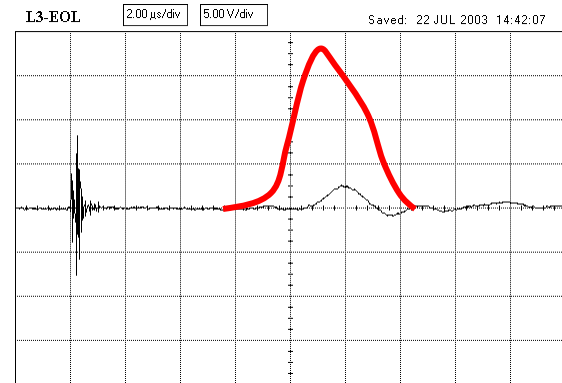
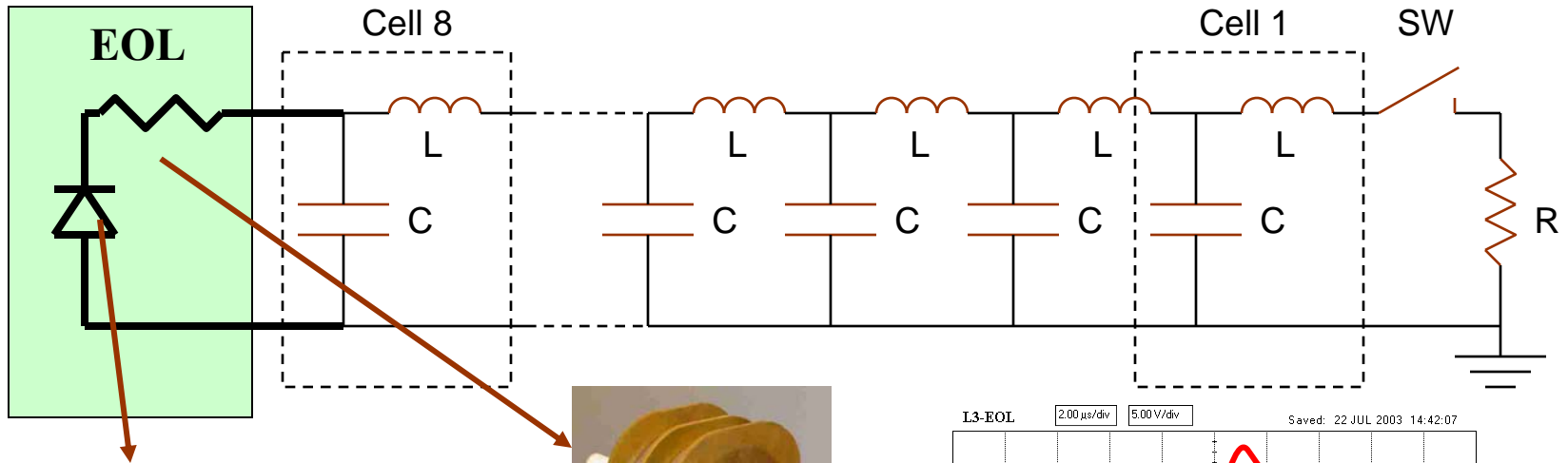
This reversed recharging has to be prevented for two reasons:

1. HV capacitors do not like noticeable voltage reversal. It shortens their lifetime.
2. Charging power supply will have to spend more time and provide higher average current to compensate reversed charges in the capacitors.

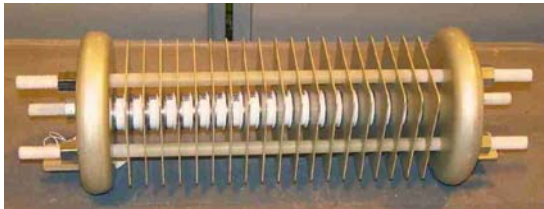


If an arc occurs during the pulse, unused portion of energy is much greater; and PFN capacitors will be charged to higher level.

# End Of Line (EOL) Clipper



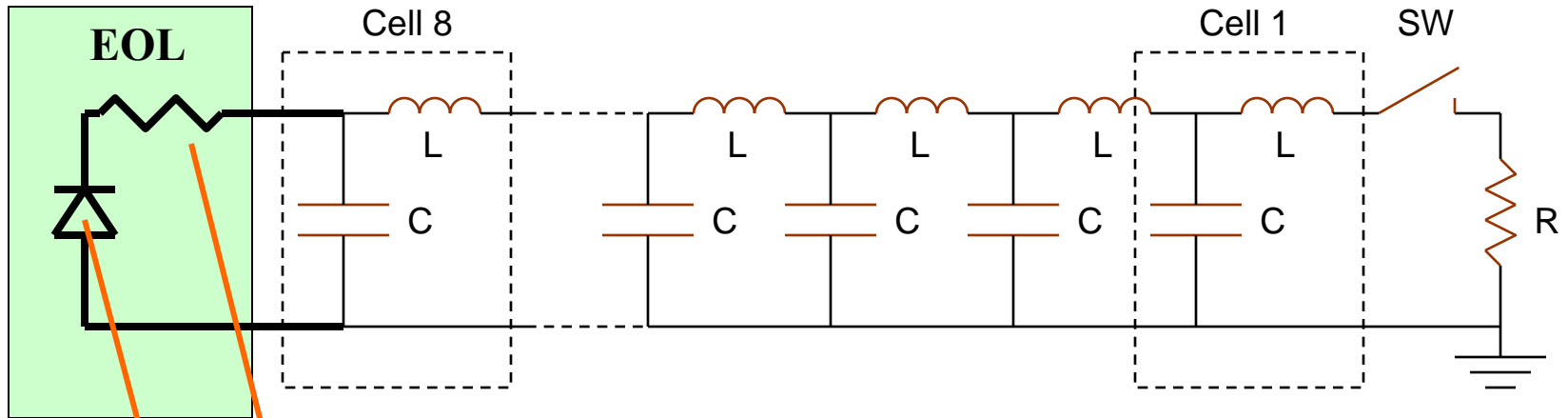
or



**EOL Clipper consists of a HV diode assembly and two sets of four 2-Ω disc resistors.**

**Diode current during a normal pulse is shown on the plot. Normally the current does not exceed few hundred amps.**

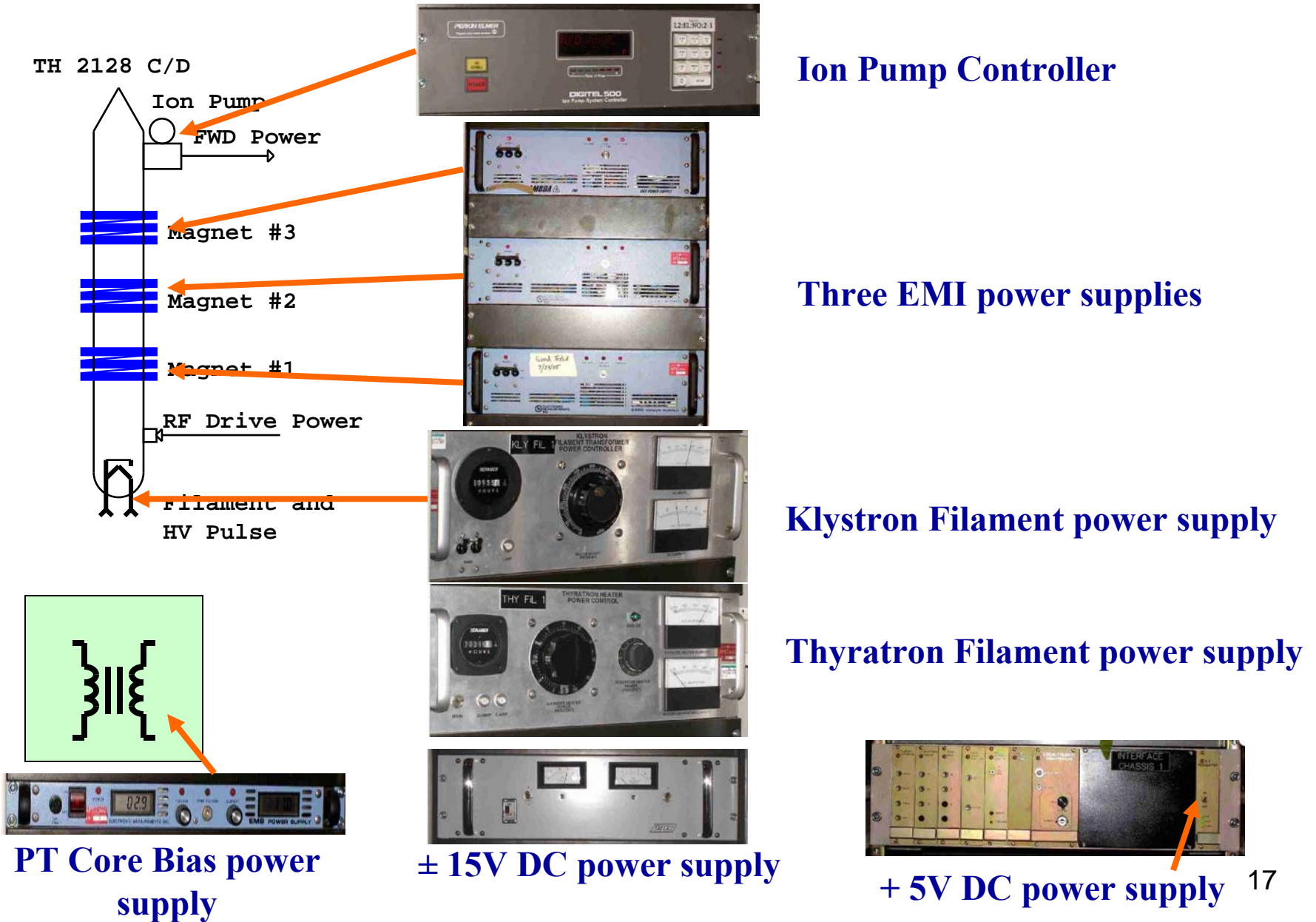
## End Of Line (EOL) Clipper



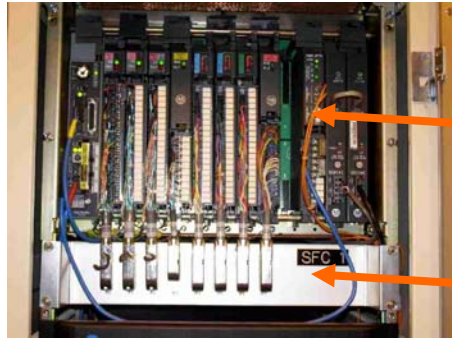
**EOL Diode current is monitored by a dedicated CT located next to the diode. The modulator Interlock and Protection system contains circuits which will stop thyatron triggering if three arcs within any one second time period are detected.**



# The Linac Modulator. Auxiliary Power Supplies



# The Linac Modulator. Control



- The modulators have Local and Remote control modes
- Local control and communication with EPICS is performed by the Allen-Bradley controller
- All input and output signals are filtered in the SFC chassis



- The touch-screen monitor makes it easy to operate the modulator, to change settings, voltage actual and current limits, etc.

